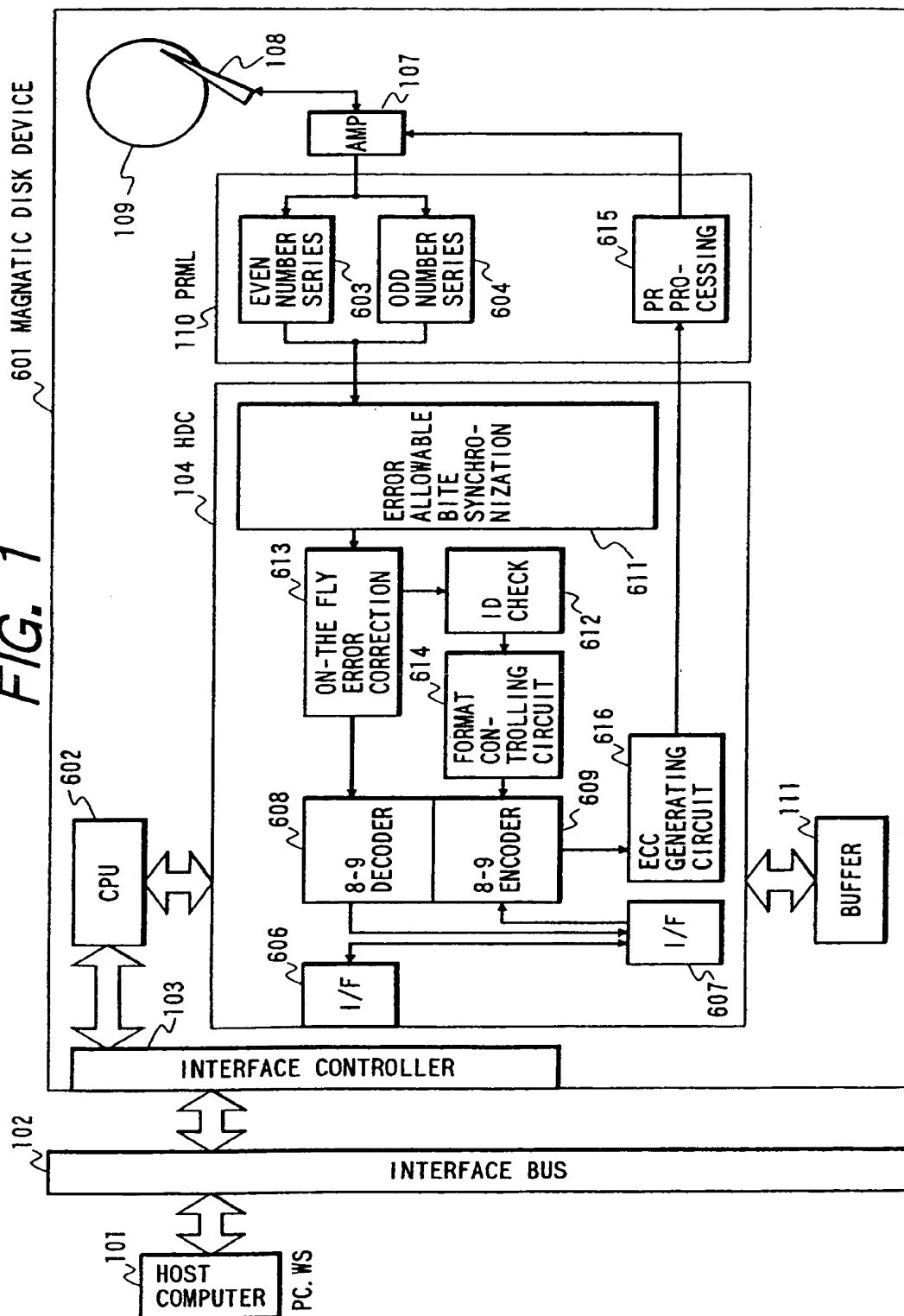
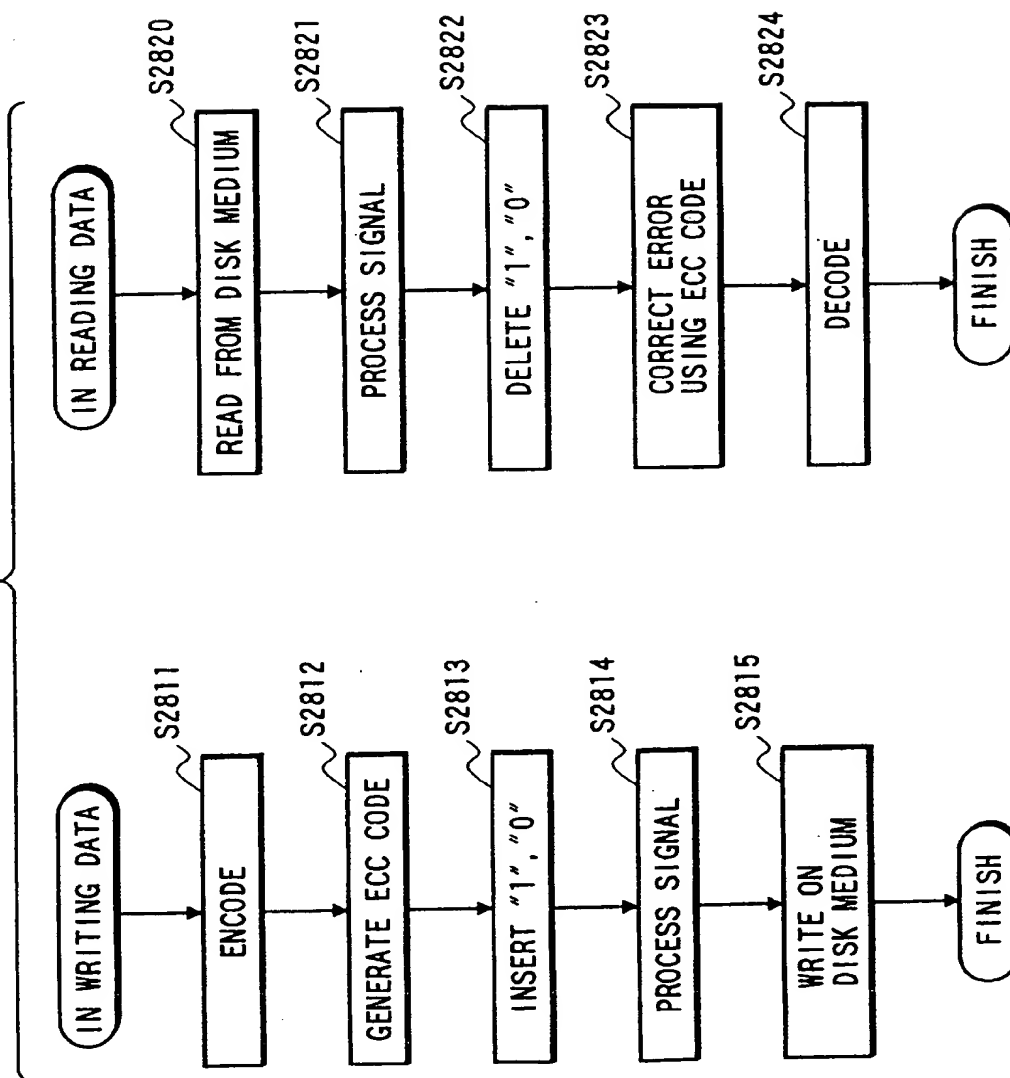


FIG. 1



002250" 516266560

FIG. 2



000000" 50000000

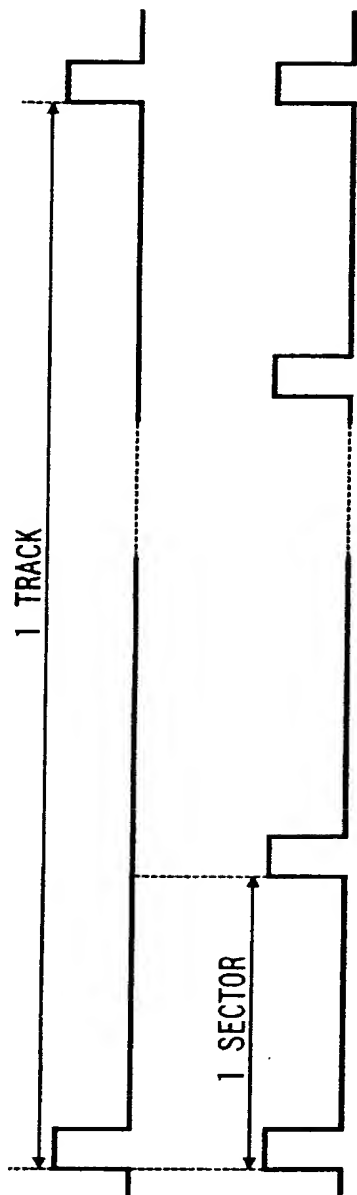


FIG. 3(a)

FIG. 3(b)



FIG. 3(c)

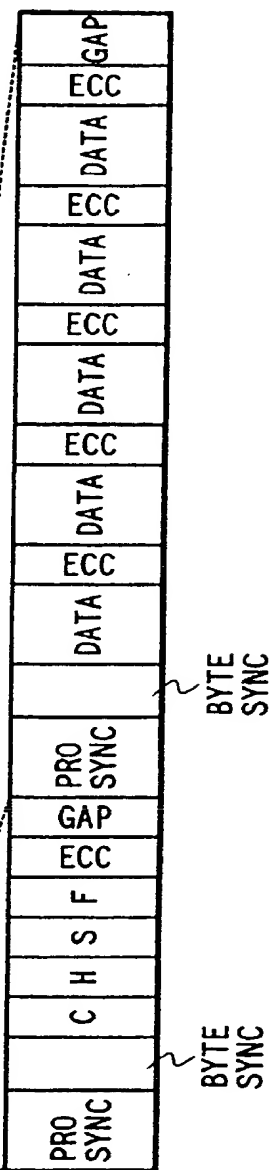
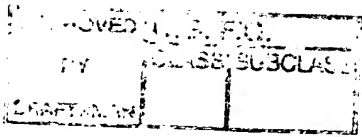


FIG. 3(d)

002230" 5626560

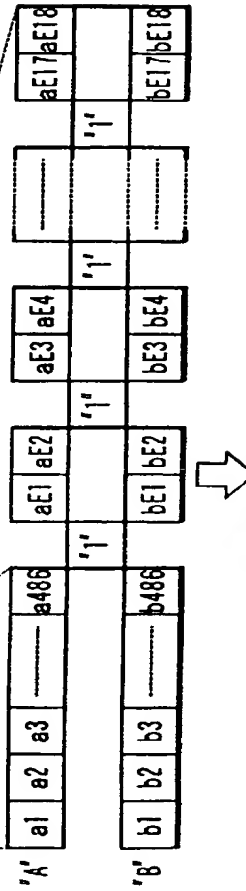
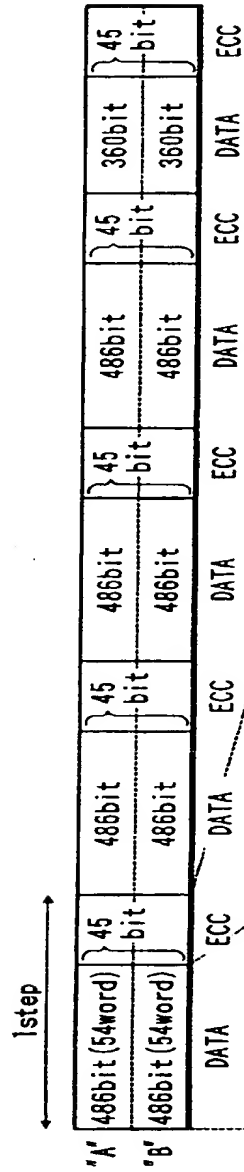
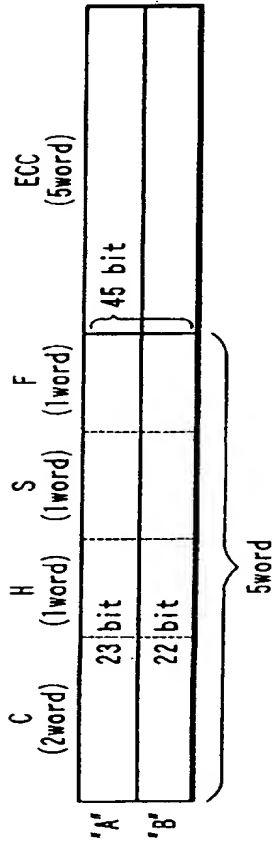


U.S. Patent

Jun. 23, 1998

Sheet 4 of 25

5,771,248



002290" 5666560

FIG. 5(a)

0 1 1 0 0 0 0 1 0 1 0 0 1 1 0 0 0 0 1 1

NO LESS THAN 0 NO MORE THAN 4

NO MORE THAN 4

0 1 0 0 0 0 1 0 0 1 0 0 1 0 0 1

1 0 0 0 1 1 0 1 0 0 0 1

ODD NUMBER SERIES
EVEN NUMBER SERIES

FIG. 5(b)

ODD NUMBER SERIES

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

NO MORE THAN 4

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

EVEN NUMBER SERIES

X : ERROR BIT
O : NORMAL BIT

FIG. 6

FIG. 7

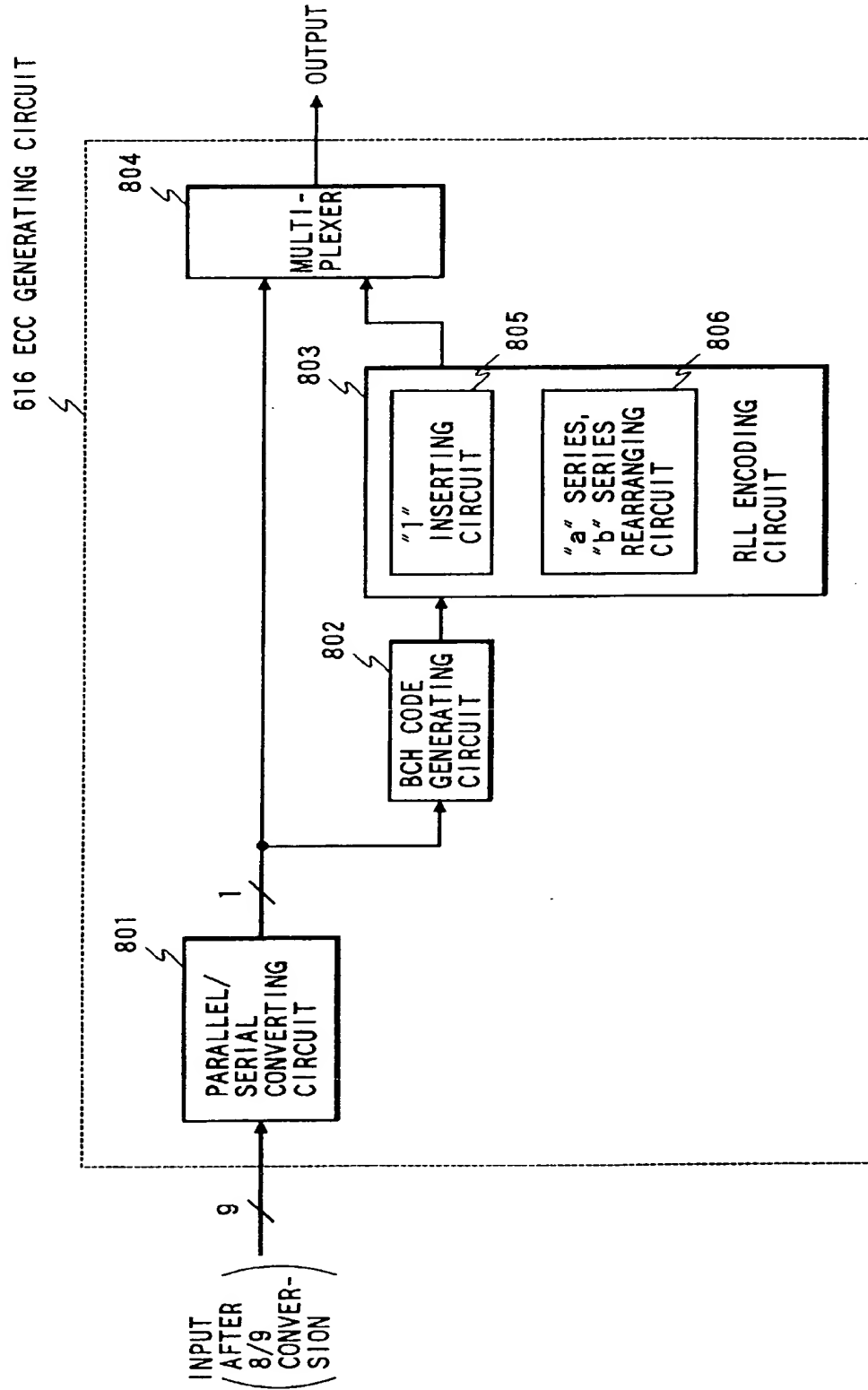


FIG. 8

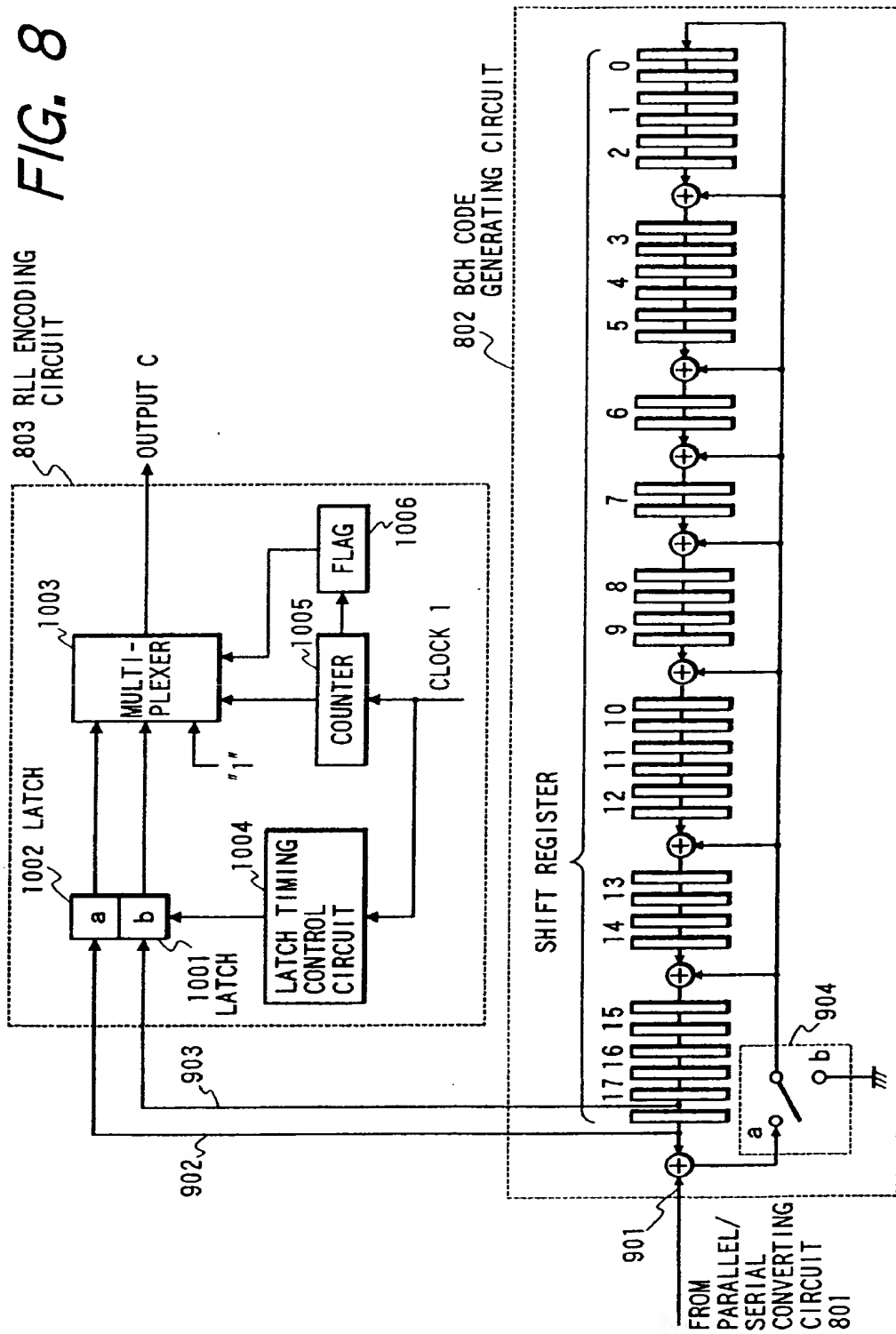


FIG. 9

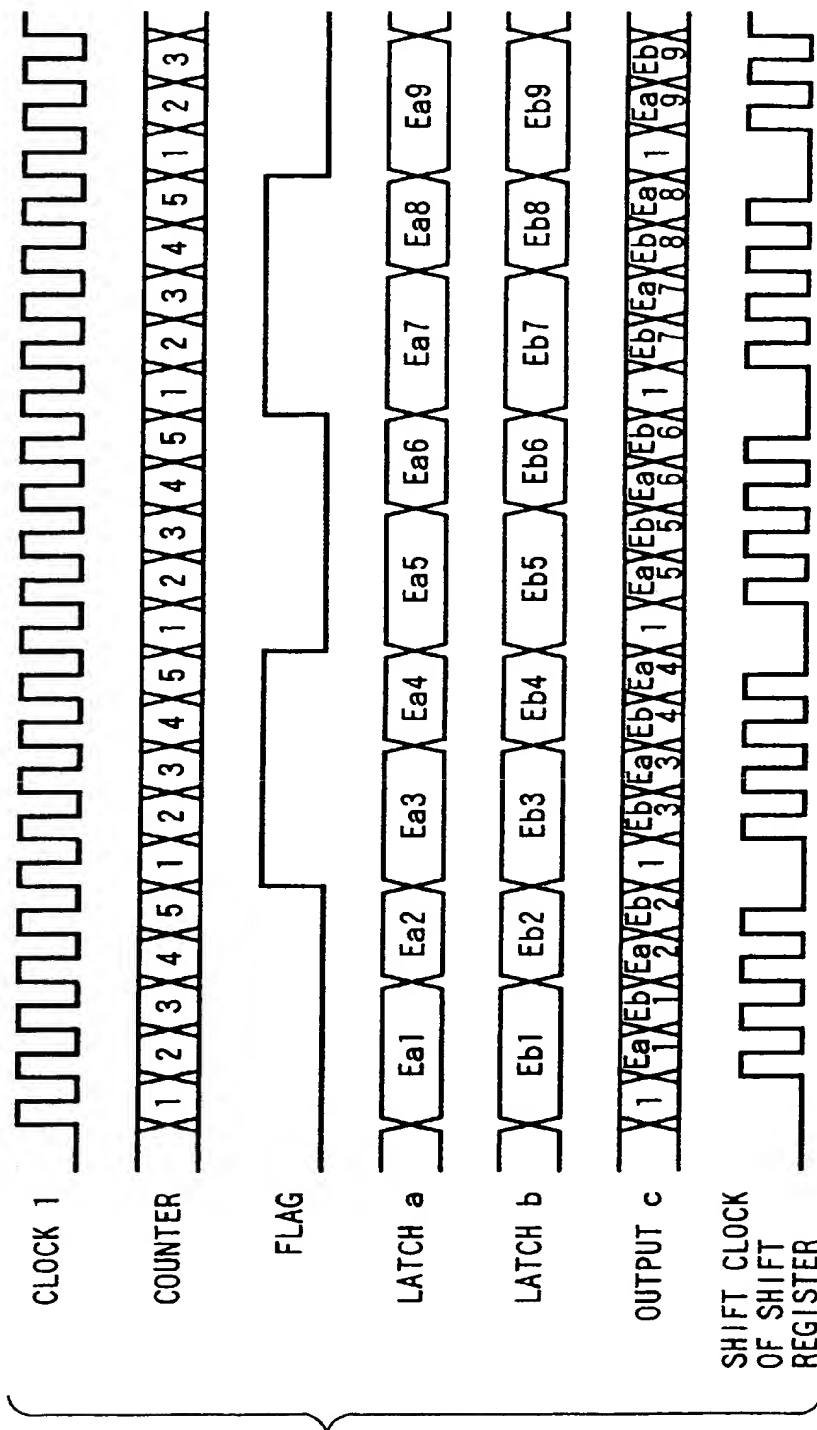


FIG. 10

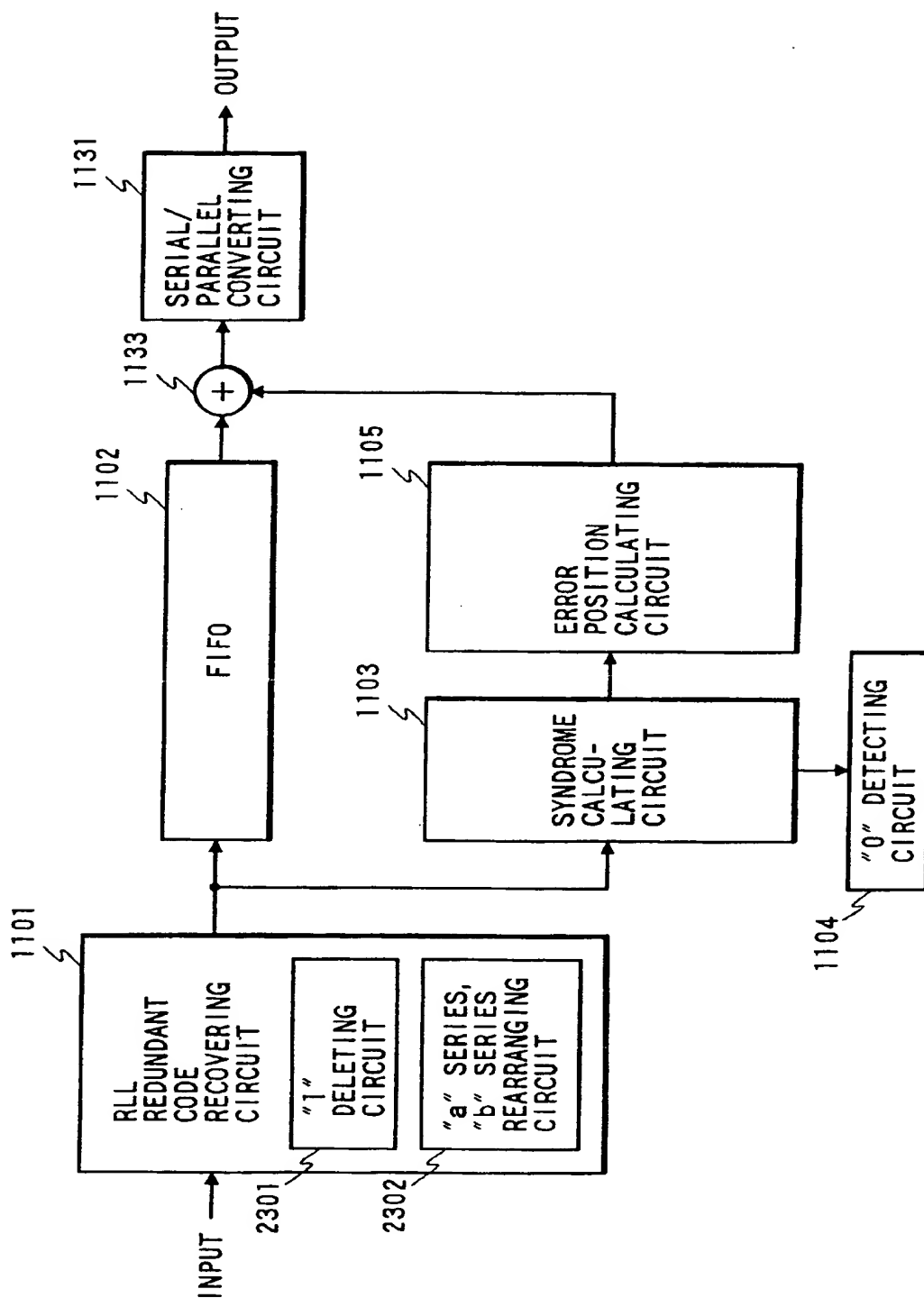


FIG. 11

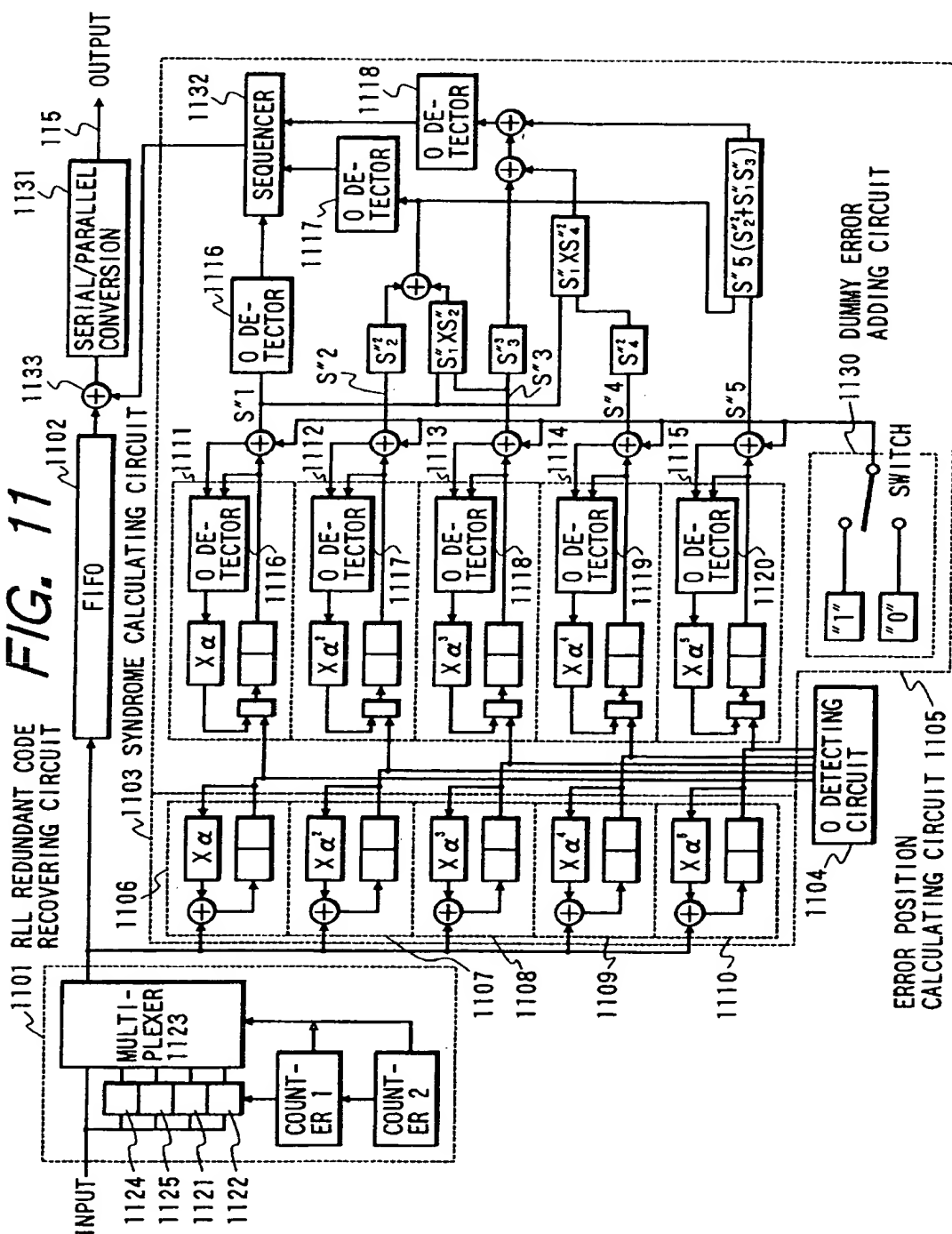


FIG. 12

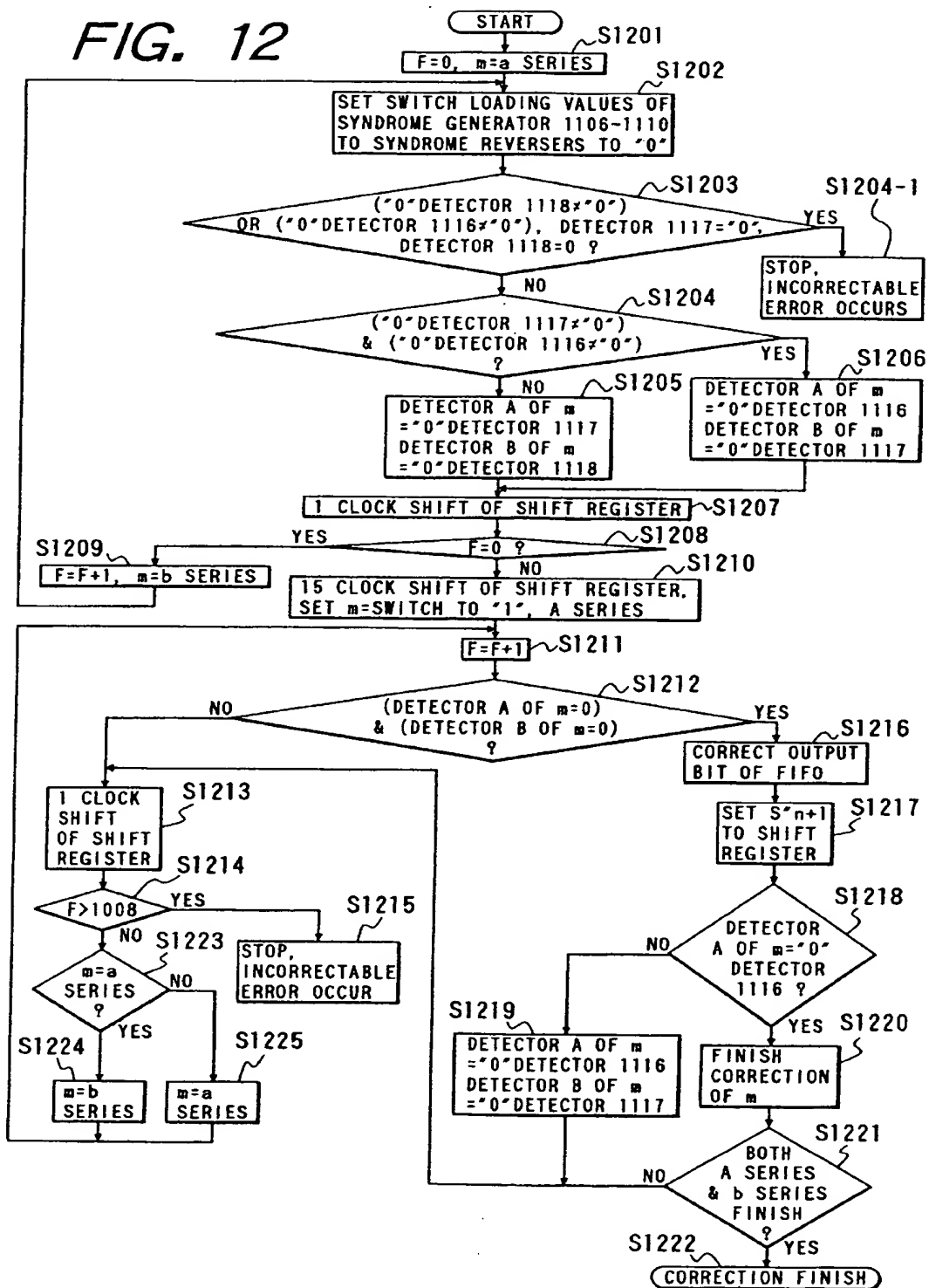
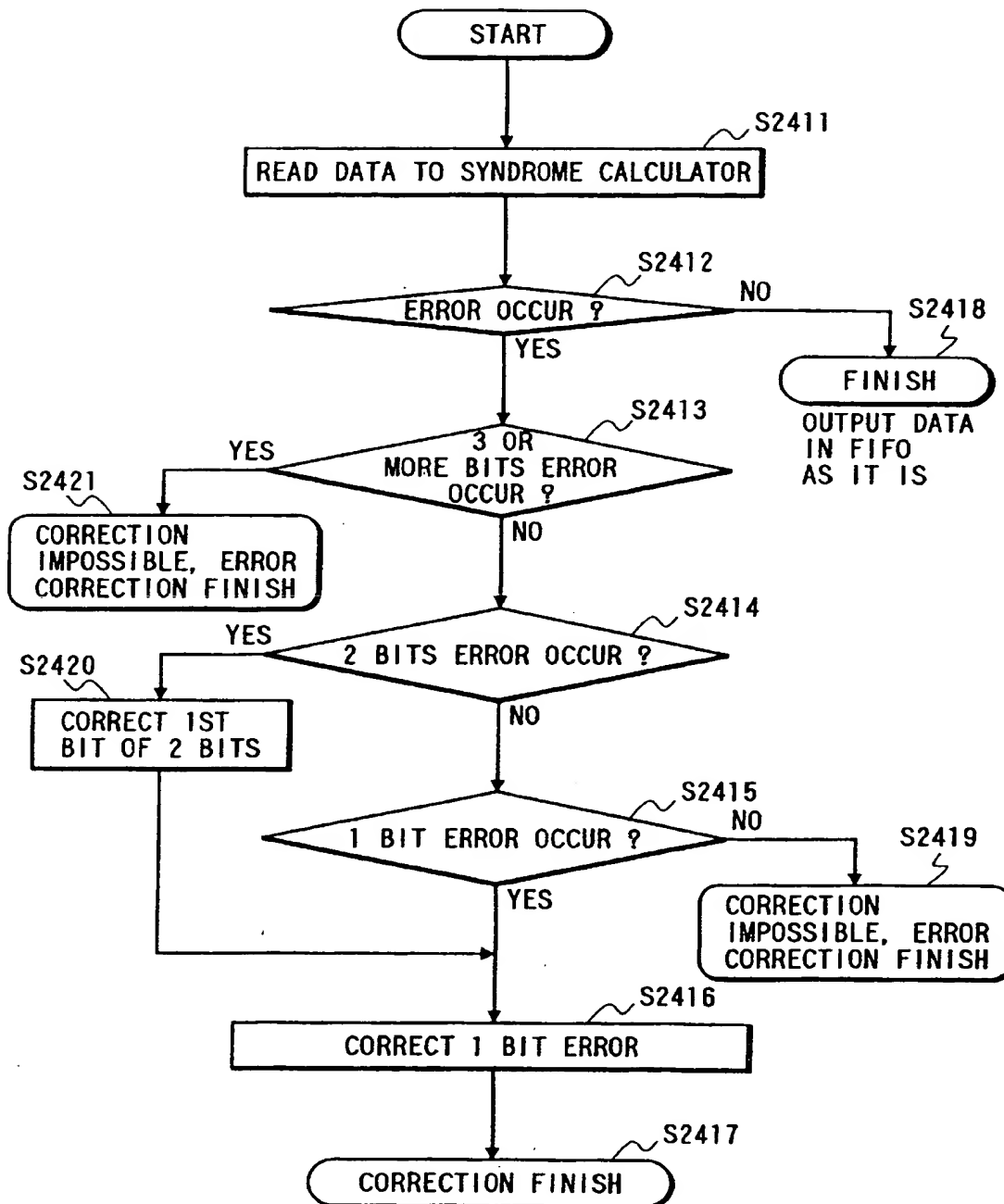
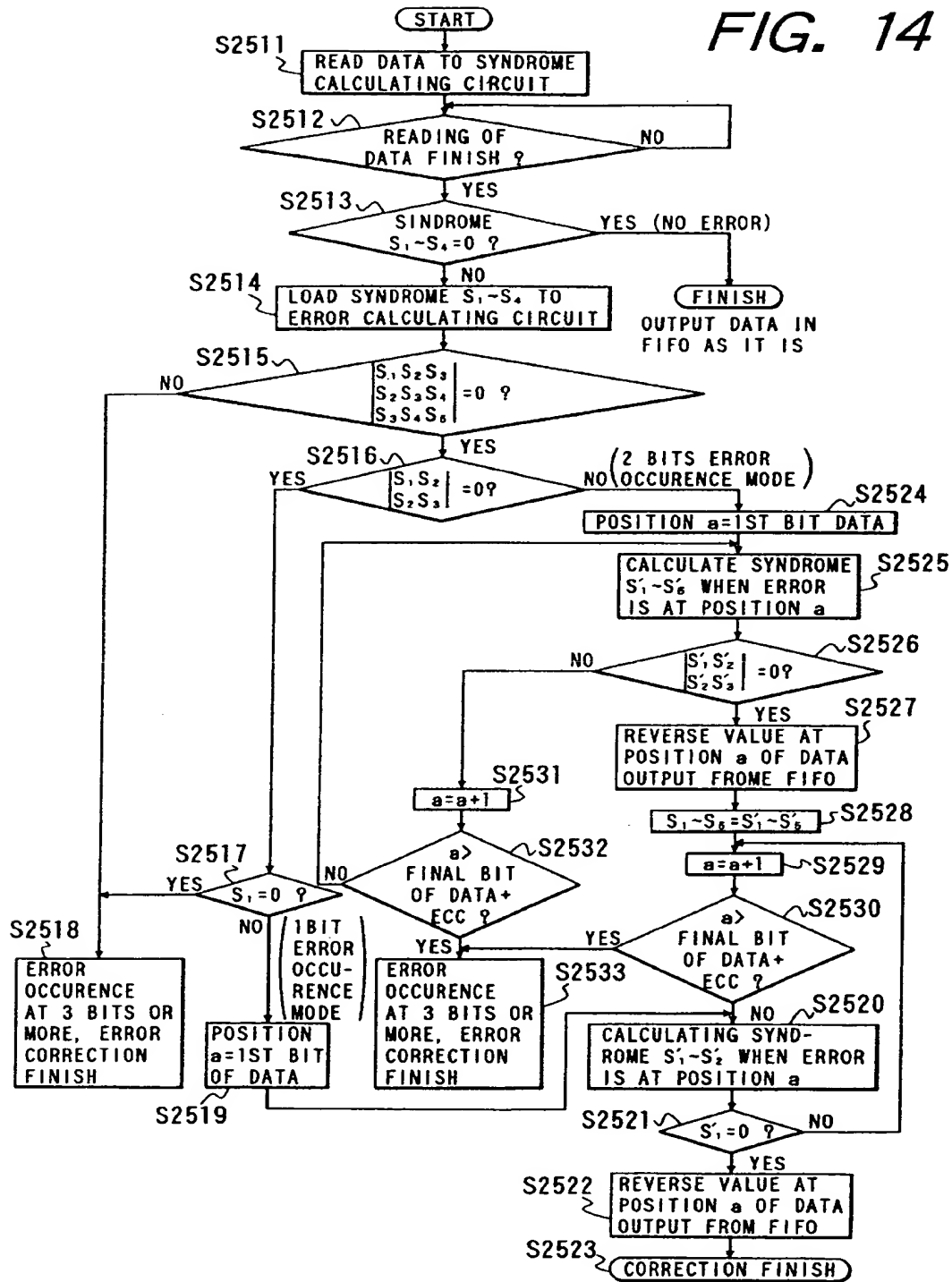


FIG. 13



002290" 56266560

FIG. 14



00230" 566560

FIG. 15

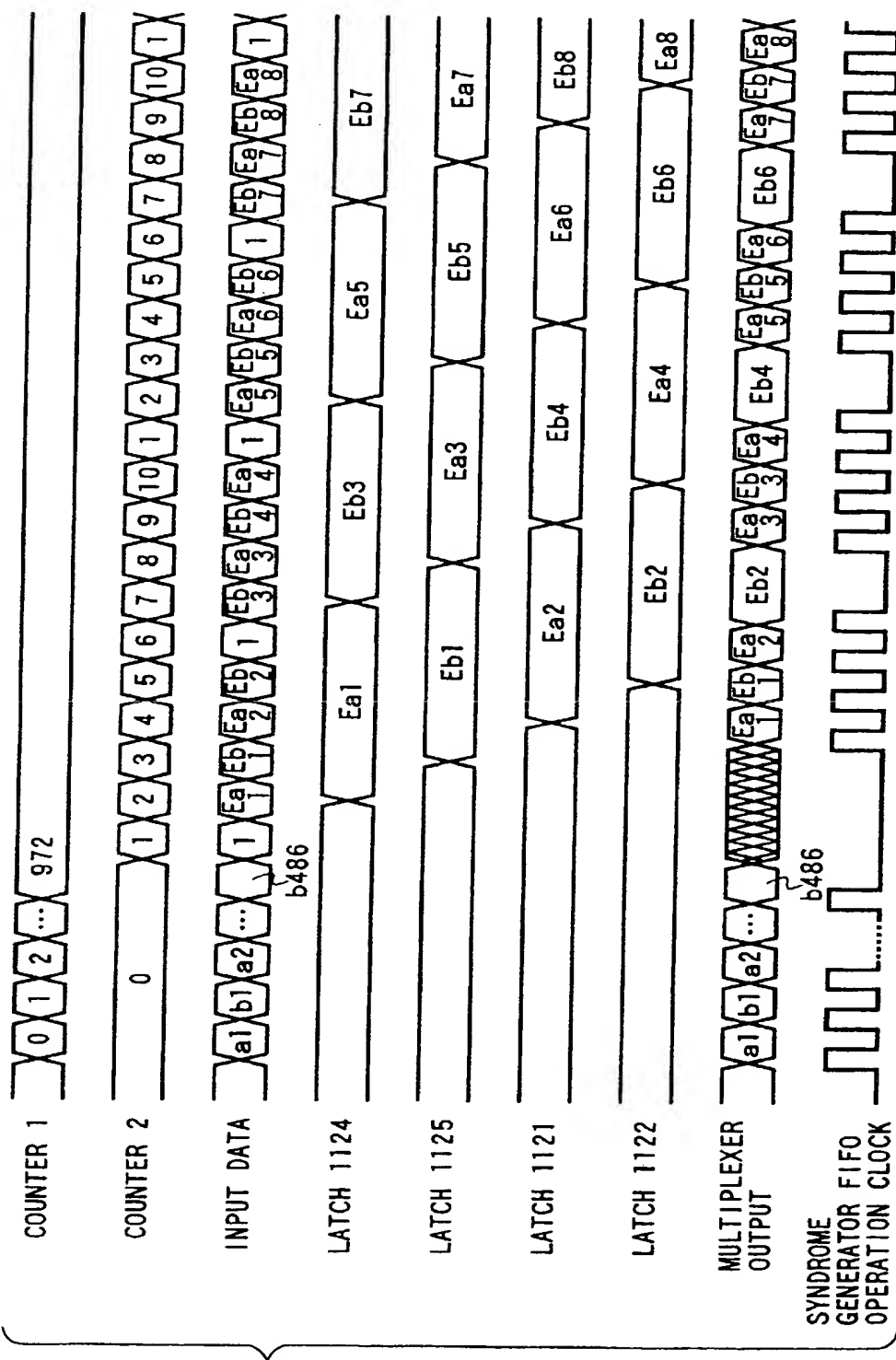




FIG. 16

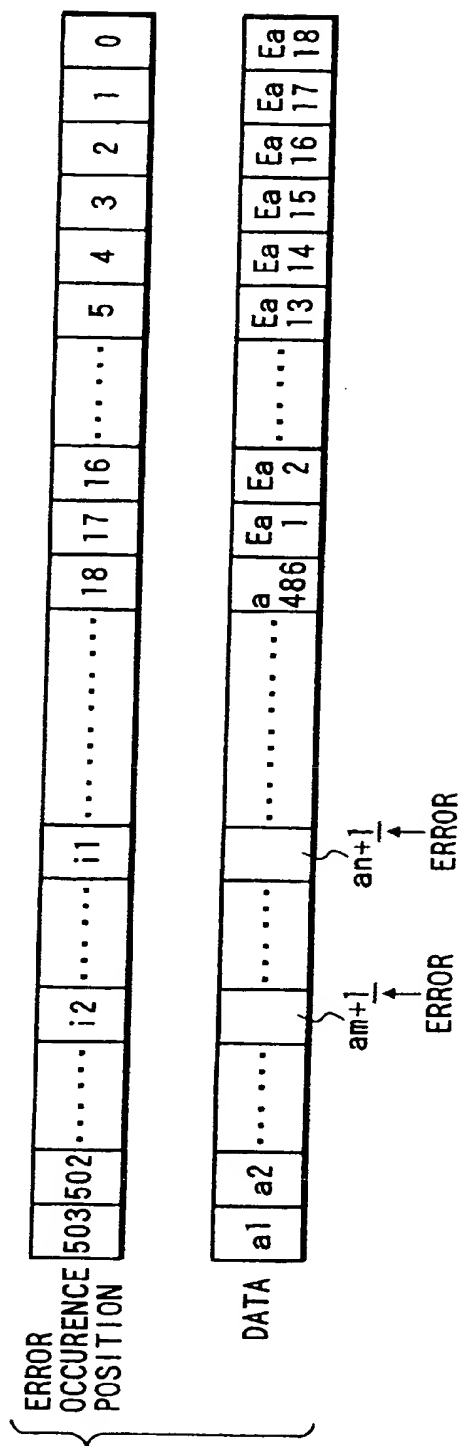


FIG. 17

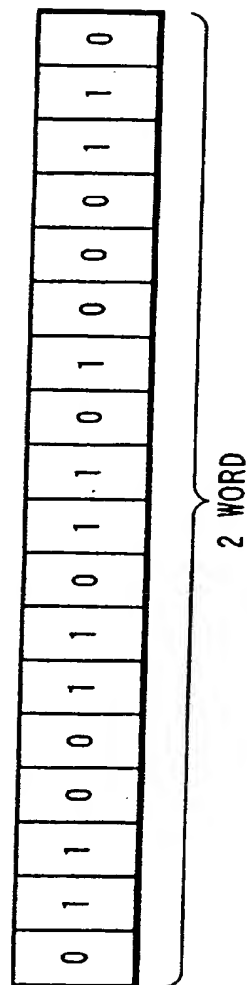


FIG. 18

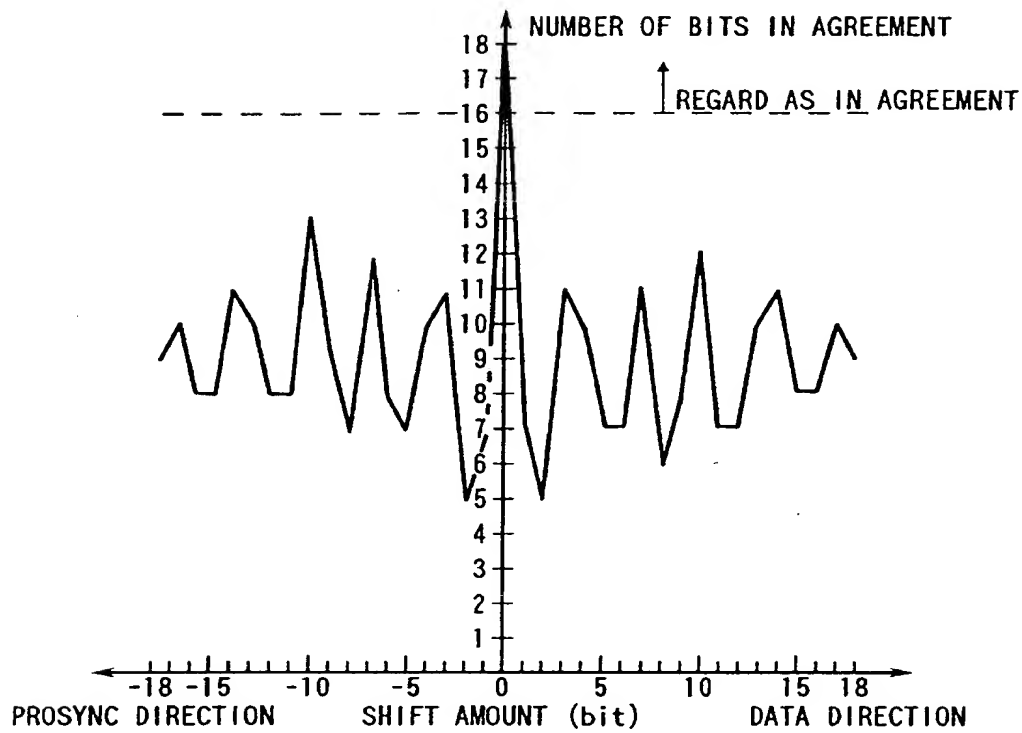
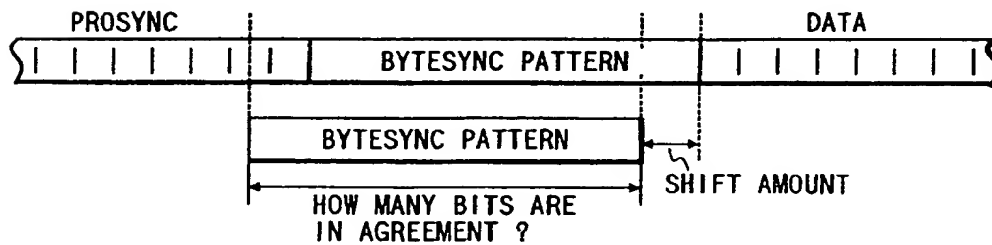


FIG. 19



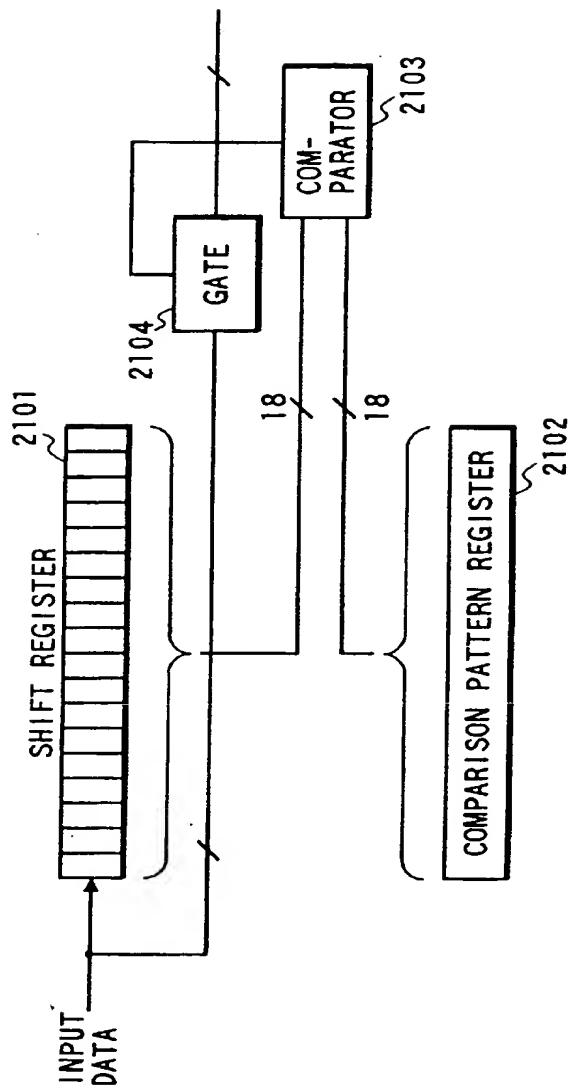


FIG. 20

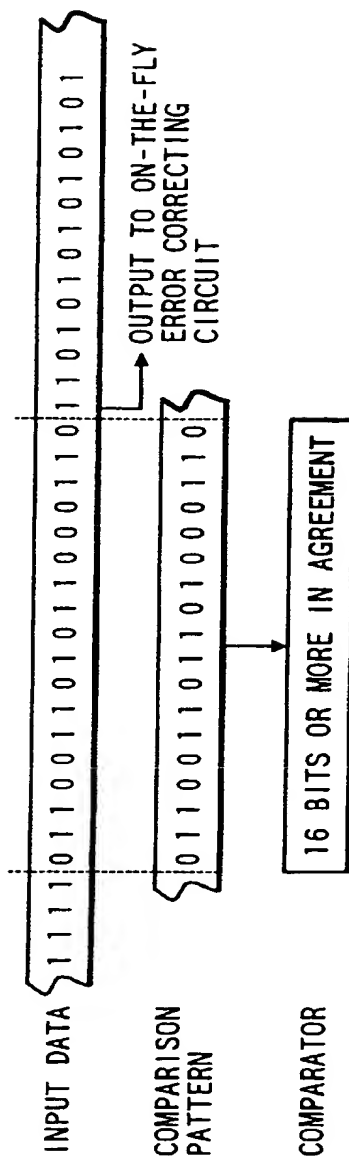


FIG. 21

FIG. 22

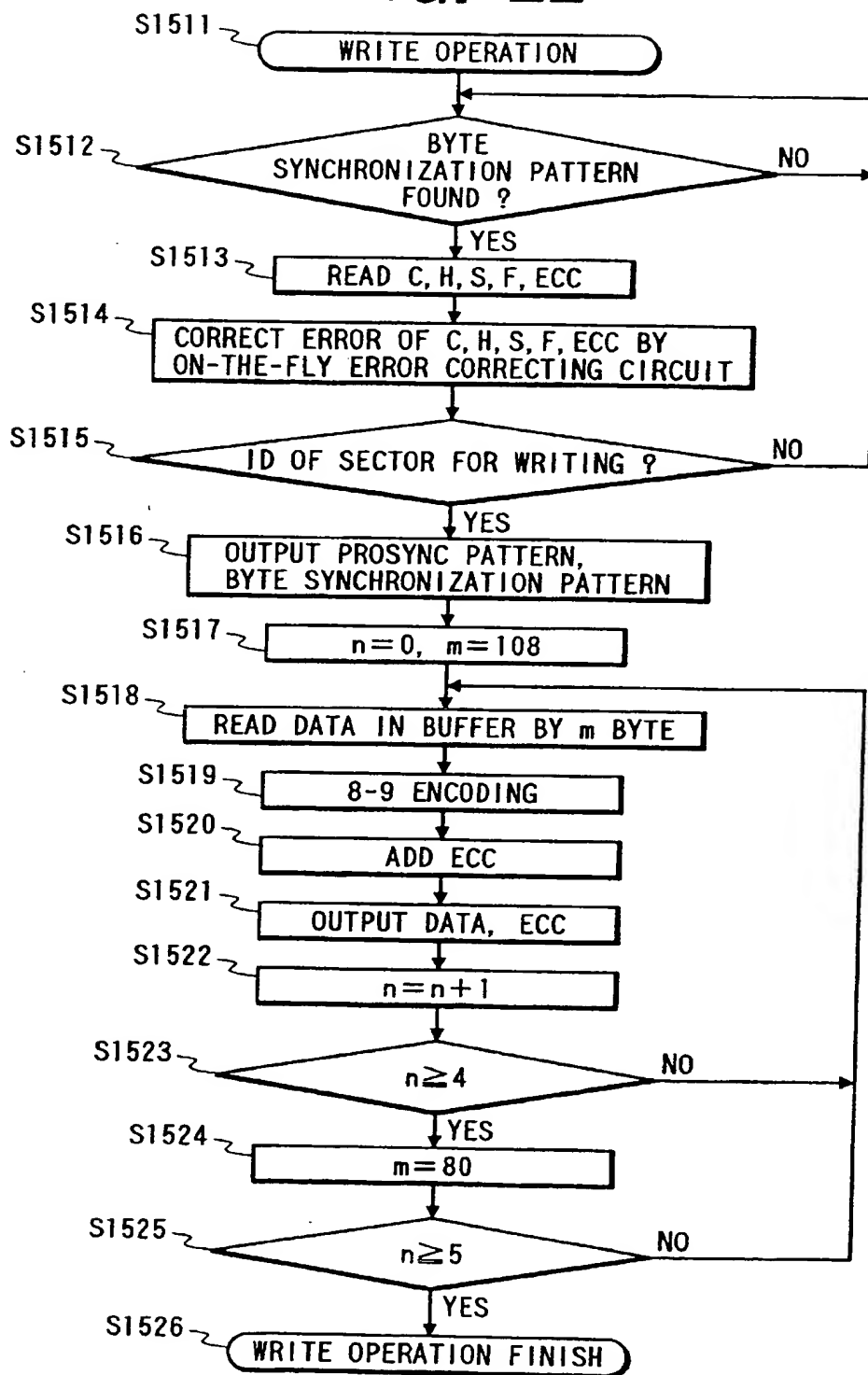


FIG. 23

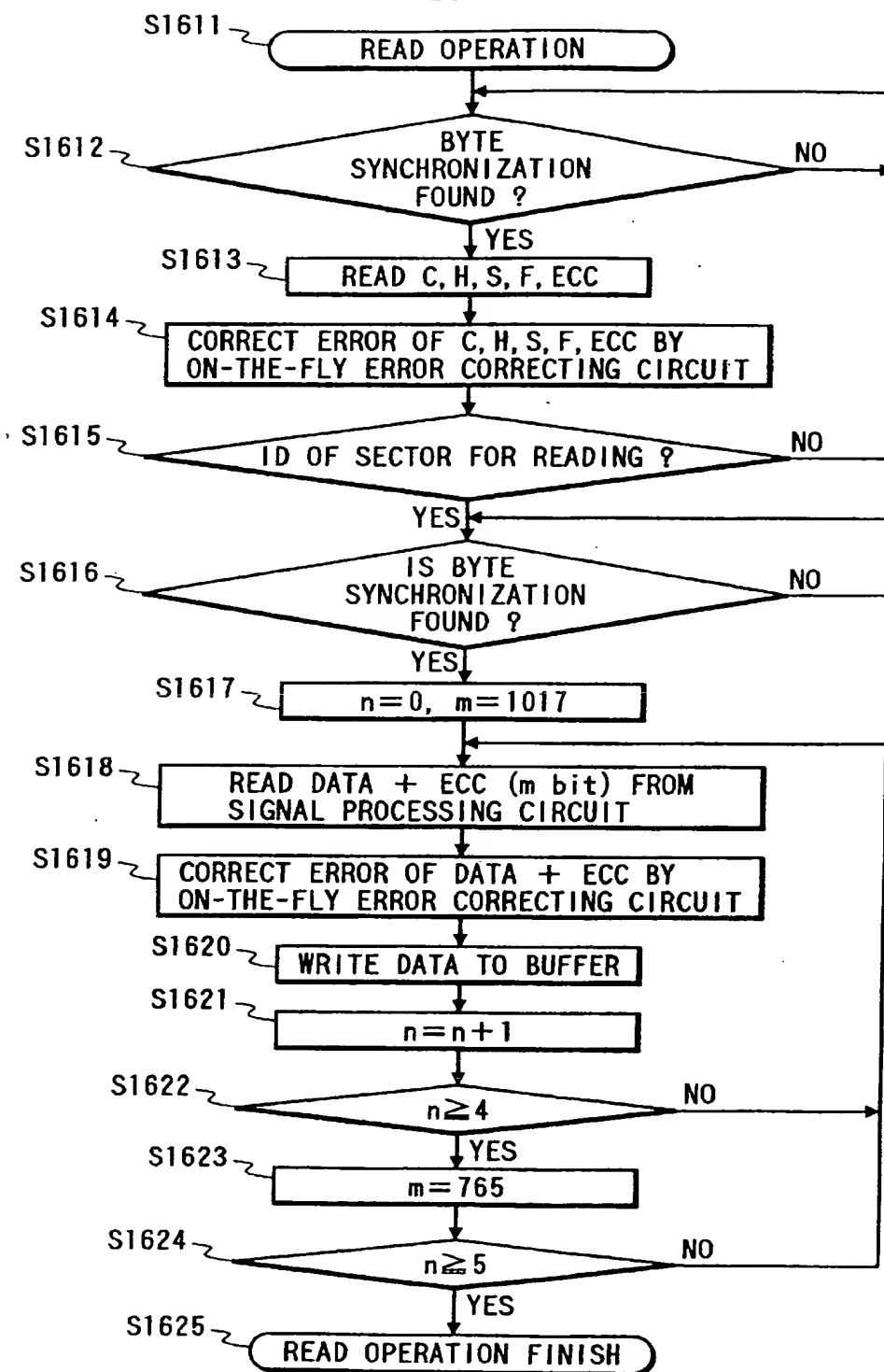
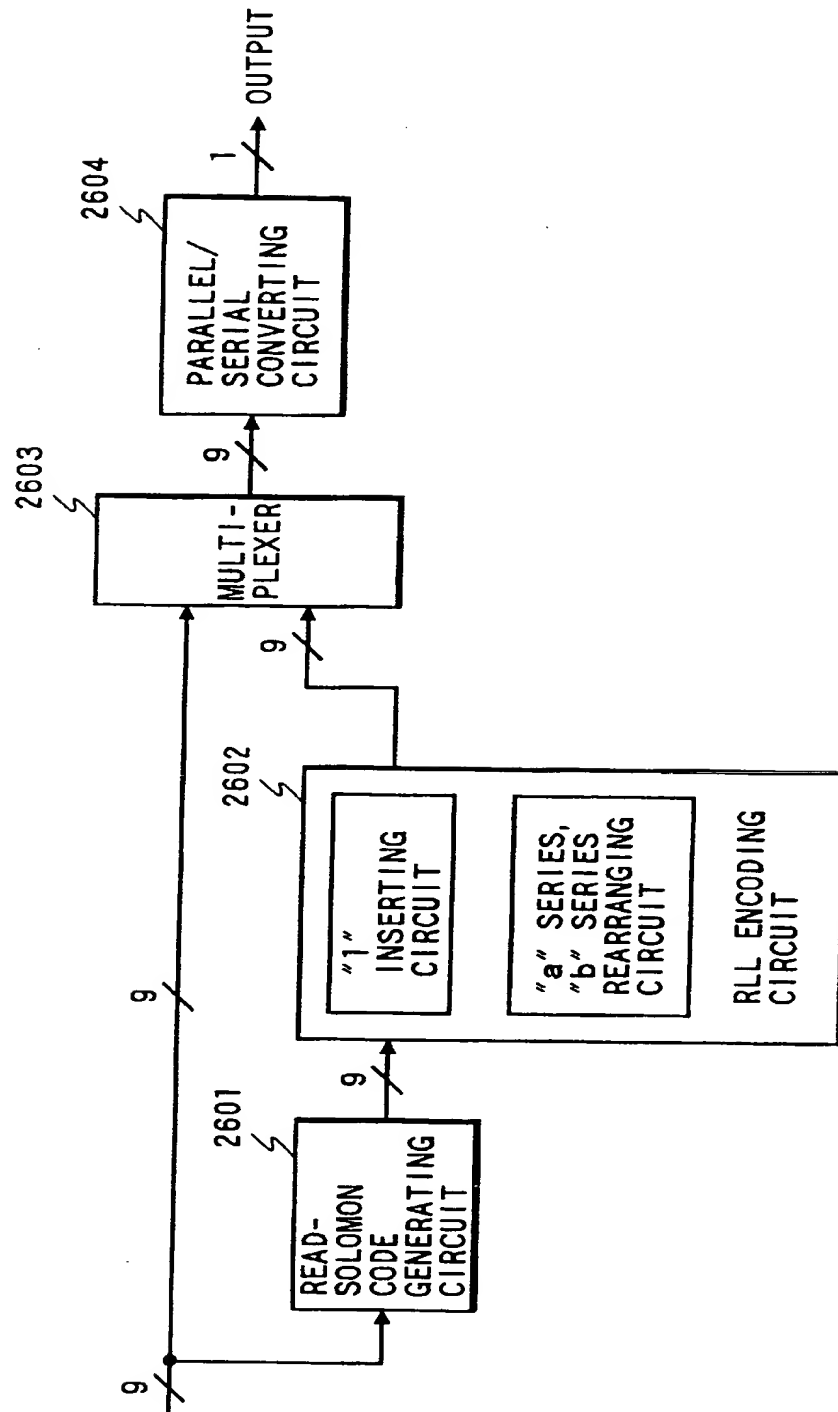
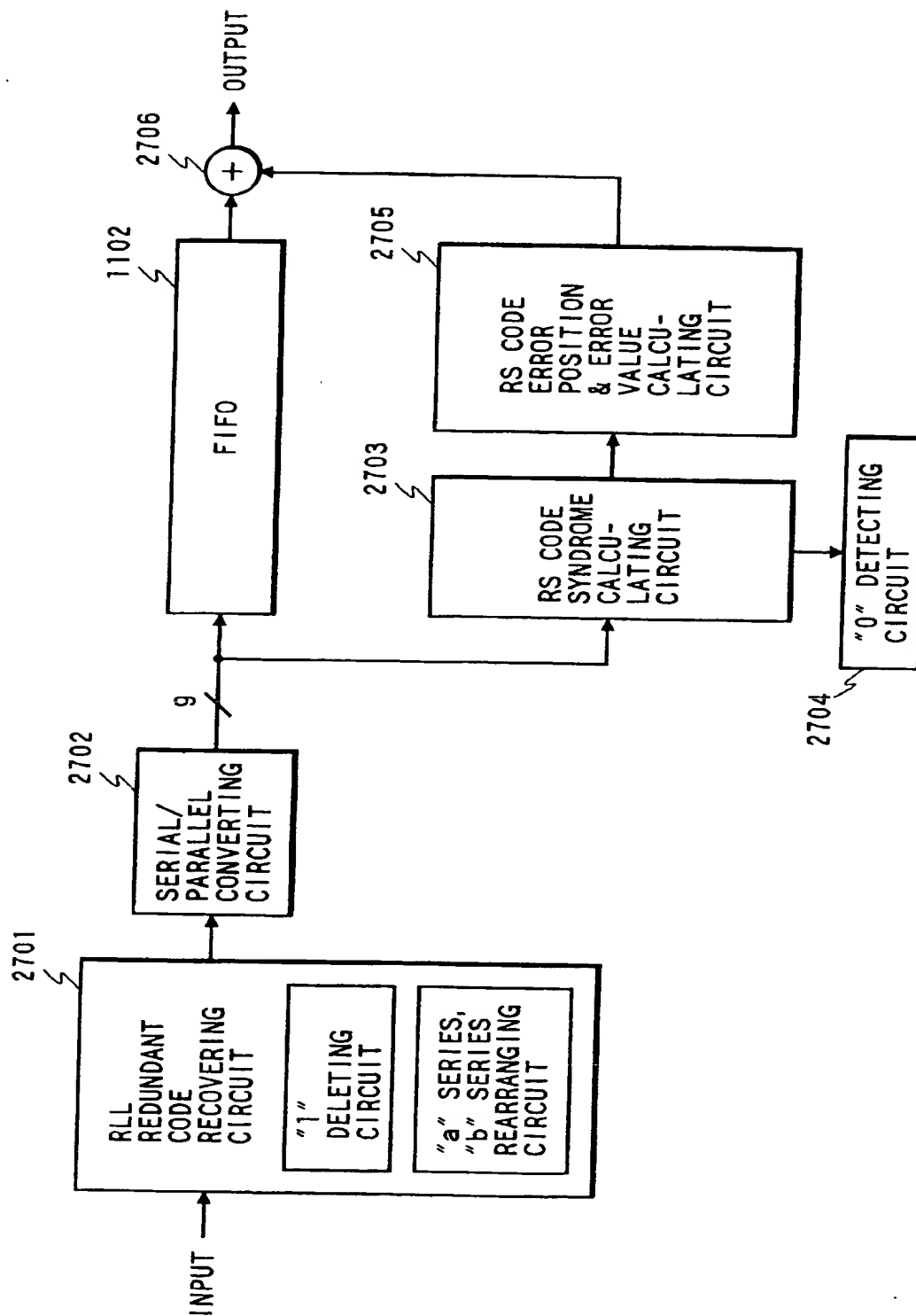


FIG. 24



002250" 56266560

FIG. 25



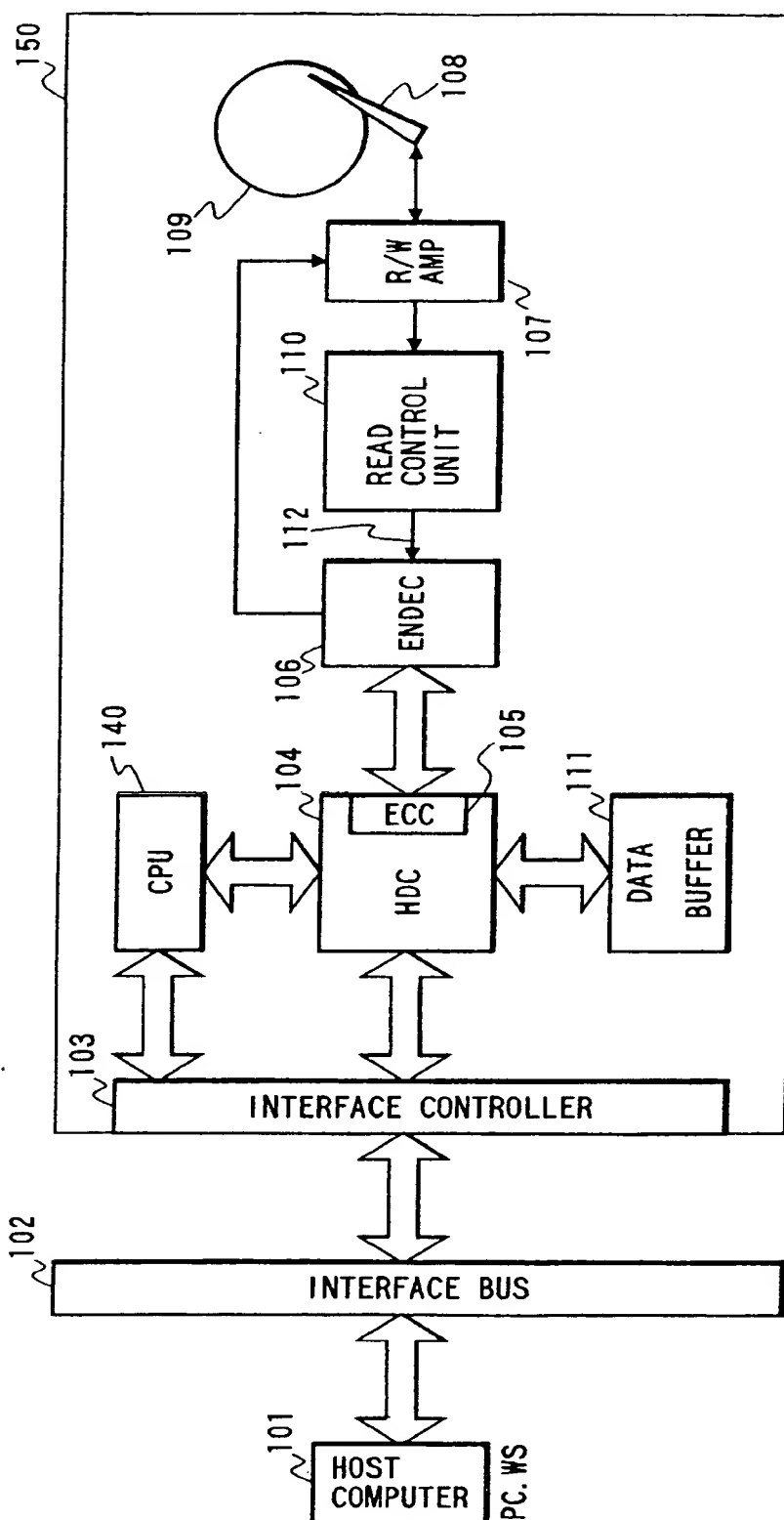
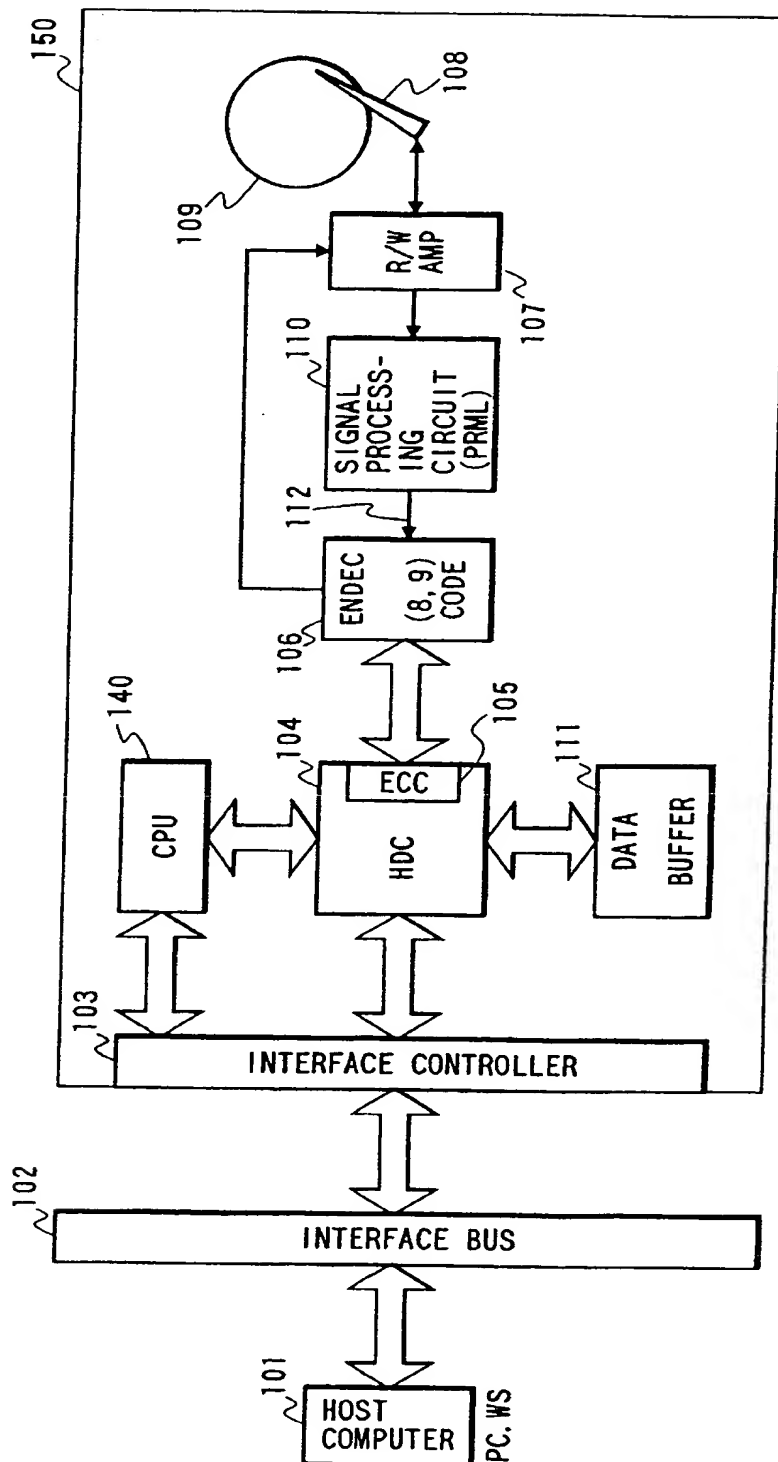
[illegible]

FIG. 27



1 TRACK 1 ROTATION

1 SECTOR LENGTH

ID	DATA	ID	DATA	ID	DATA	ID	DATA	ID	DATA
----	------	----	------	----	------	----	------	----	------

PRO SYNC	BYTE SYNC	ID NO.	CRC	GAP	PRO SYNC	BYTE SYNC	DATA	ECC
----------	-----------	--------	-----	-----	----------	-----------	------	-----

PROSYNC	:	REGION OF GENERATING SYNCHRONIZATION CLOCK FOR READING
BYTESYNC	:	PATTERN FOR ESTABLISHING BYTE SYNCHRONIZATION
ID NUMBER	:	REGION OF STORING ID NO. INHERENT TO EACH SECTOR
CRC	:	ERROR CHECK CODE
GAP	:	INEFFECTIVE REGION NECESSARY FOR DETERMINING
	:	ALLOWABILITY OF WRING DATA AFTER READING ID
DATA	:	DATA
ECC	:	ERROR CORRECTING CODE

